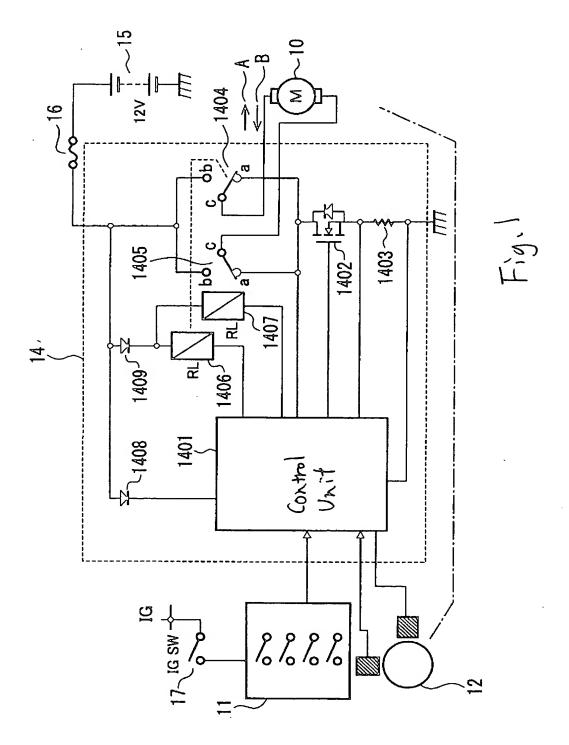
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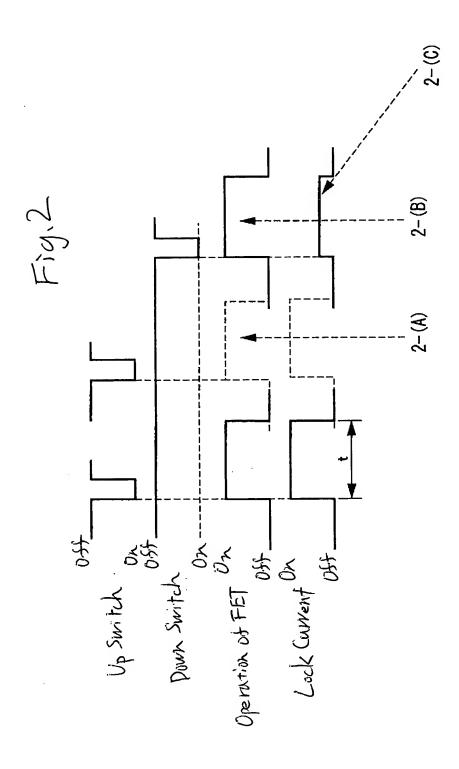
一図面

【図1】



3

【図2】

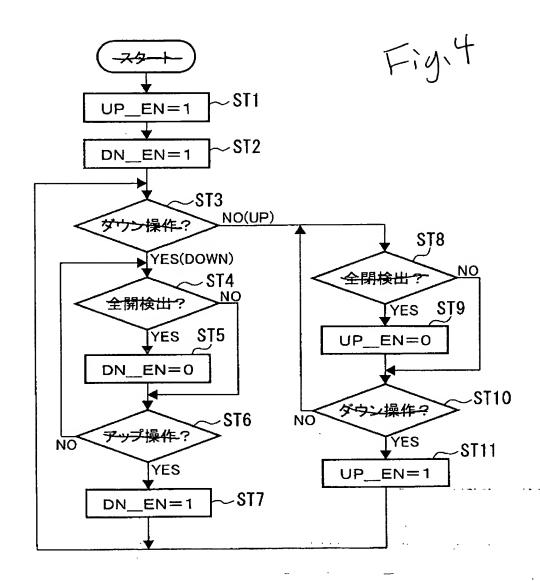


<u>= P 0 4 2 2 0 3</u>

特願2003

【図3】 FET Gate FIET Garte CURRENT MONITOR/FOLLOW-UP CIRCUIT Down RL 마 14019 14018 4020 ON/OFF CONTROL CIRCUIT 1401 WATCHDOG CIRCUIT 然被回路 治統回路 14015 RELAY-DRIVER. 14021 GATE DRIVER 14017 **筆争ロッシショ報** クペーナイベナウ 臨 ナン・イン・生物画語 14017 Holq,14018 14020 14021 14022 14022 -14011 14014 14013 PULSE INPUT PROCESS/COUNTER CIRCUIT いまイイセが Battery Voltage 印斯回路 野回木中 STATE STATE 四四四 14012 CHATTERING PROCESS CIRCUIT POWER-ON RESET CIRCUIT CONTROL LOGIC CIRCUIT 5-V REGULATOR CIRCUIT OSCILLATION CIRCUIT Auto-D Pulse2 **Pulse**1 Down Auto д П ß SETTING OF LIMIT POSITION SETTING OF PULSE KIND SETTING OF PULSE PITCH: م MS 5I 0 14014 14015 14012 14013 14016 14011

【図4】

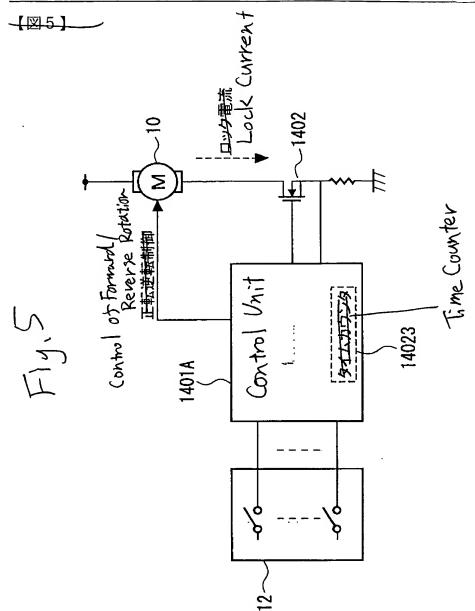


START

- ST3 LOWERING OPERATION?
- ST4 FULL-OPENING IS DETECTED?
- ST6 RAISING OPERATION?
- ST8 FULL-CLOSING IS DETECTED?
- ST10 LOWERING OPERATION?

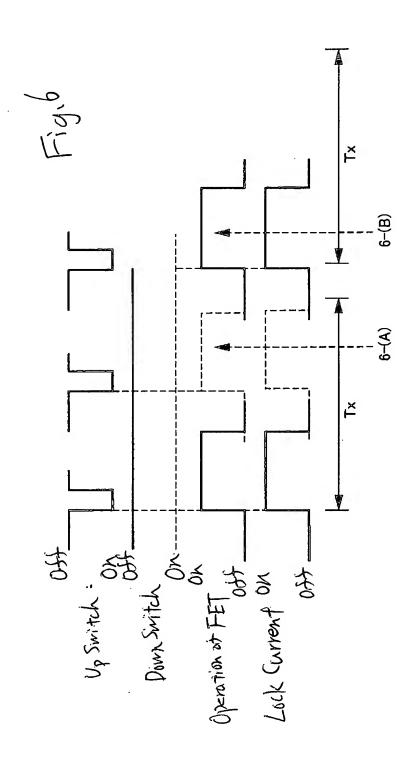
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Darryl Mexic 202-293-7060
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整理番号=P042203



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【図6】



Filing Date: April 5, 2004 Darryl Mexic 202-293-7060 START 7 of 9 整理番号=P042203一 ST23 LOWERING OPERATION? ST24 FULL-OPENING IS DETECTED? スタ ST26 RAISING OPERATION? ST28 -ST20 TIME OVERFLOWS? Time=0 ST31 FULL-CLOSING IS DETECTED? -ST21 UP EN=1ST33 LOWERING OPERATION? -ST22 DN EN=1**ST35** TIME OVERFLOWS? ST23 NO(UP) ダウン操作や **ST31** YES(DOWN) NO 全閉検出? ST24 NO 全開検出? YES -ST32 YES UP_EN=0 ST25 DN_ EN=0**ST33 ST26** YES ダウン操作? アンプ操作・ NO(UP) NO(DOWN) Time++ Time++ **ST34 ST35** Time **ST27 ST28** バフロ NO Time -パフロ-YES NO YES -ST36 Time = 0-ST29 Time=0 -ST37 UP_ EN=1**ST30** EN=1DN

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